UNIVERSITY OF SOUTH FLORIDA

Defense of a Master's Thesis

Protocol Guided Trace Analysis for Post-Silicon Debug under Limited Observability
by
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For the MSCS degree in Computer Science & Engineering

This thesis considers the problem of reconstructing system-level behavior of an SoC design from a partially observed signal trace. Solving this problem is a critical activity in post-silicon validation, and currently depends primarily on human creativity and insights. In this thesis, we provide an algorithm to automatically infer system-level transactions from incomplete, ambiguous, and noisy trace data. This thesis also demonstrates the approach on two multicore virtual platforms.

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THE PUBLIC IS INVITED

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